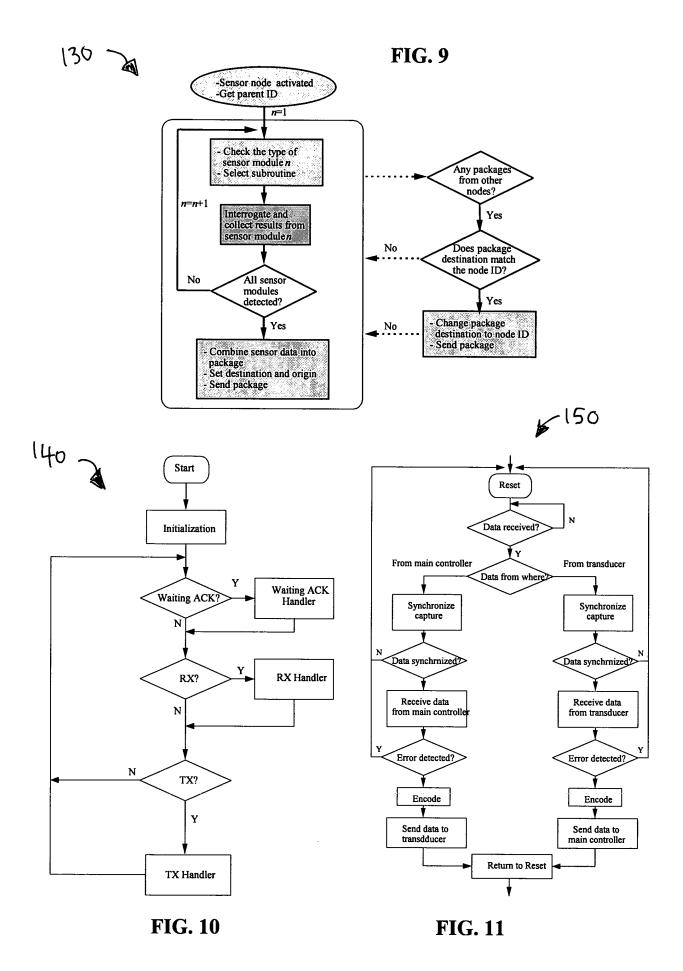
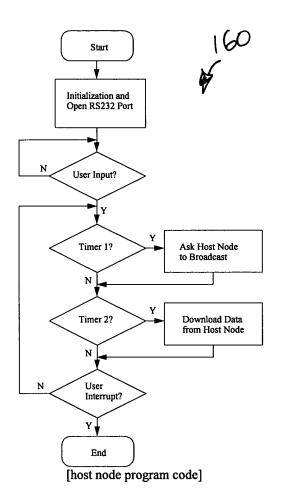


FIG. 8

Header Data Field

Destination Origin Sensor 1 Results Sensor 2 Results





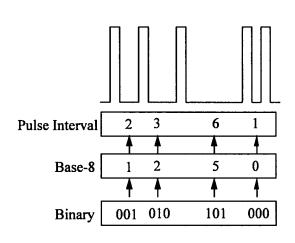
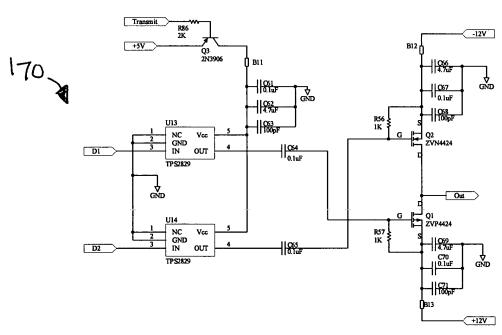
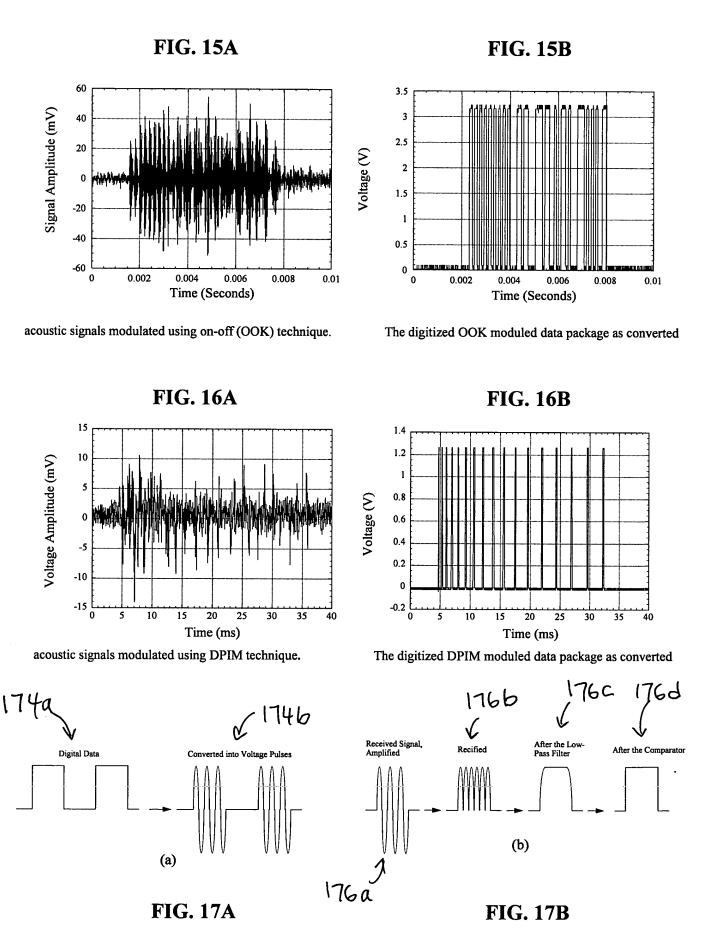


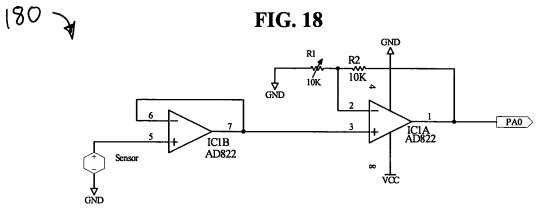
FIG. 12 FIG. 14

**FIG. 13** 

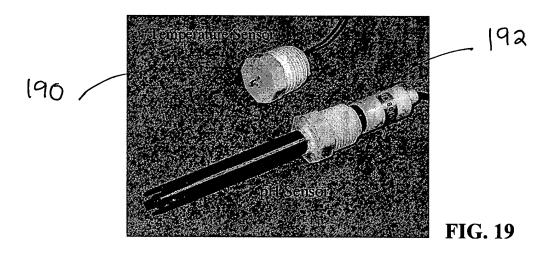


Circuitry to convert digital signals from the [main] node assembly controller to voltage pulses for the transducer.





sensor interface circuitry for potential-based sensors (e.g., thermistors)



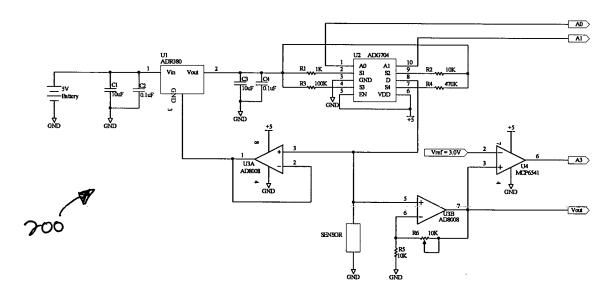
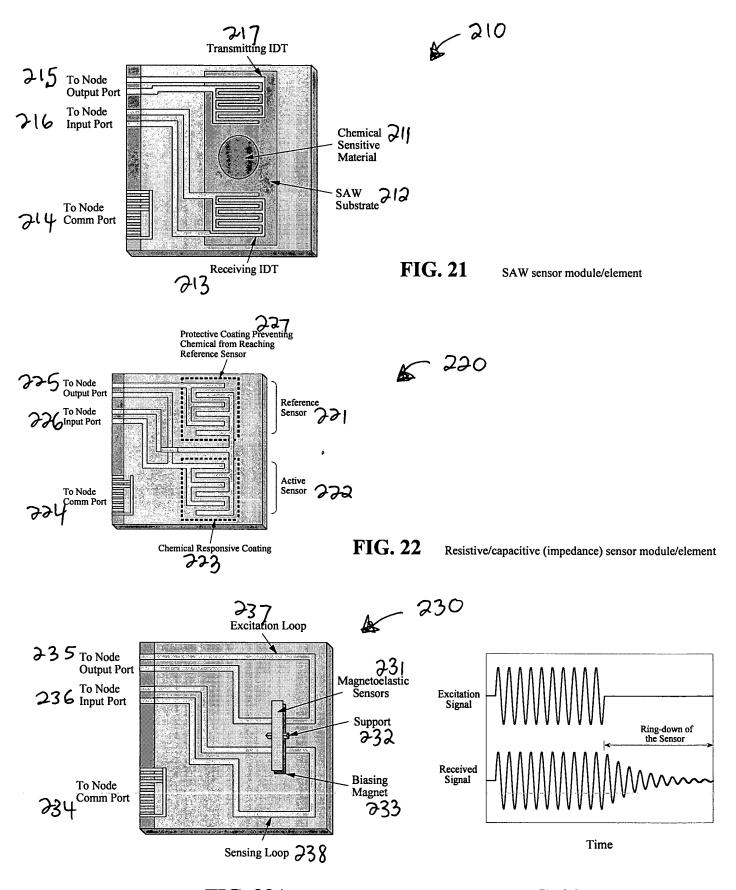


FIG. 20 sensor interface circuitry for resistive sensors.



**FIG. 23A** 

**FIG. 23B** 

